

Data Sheet



Description

The FSS-343 series Photocoupler is ideally suited for driving power IGBTs and MOSFETs used in motor control inverter applications and inverters in power supply system. It contains an LED optically coupled to an integrated circuit with a power output stage.

The 4.0A peak output current is capable of directly driving most IGBTs with ratings up to 1200 V/200 A. For IGBTs with higher ratings, the FSS-343 series can be used to drive a discrete power stage which drives the IGBT gate.

The Photocoupler operational parameters are guaranteed over the temperature range from -40°C ~ +110°C.

Features

- 4.0 A maximum peak output current
- · Rail-to-rail output voltage
- 110 ns maximum propagation delay
- Under Voltage Lock-Out protection (UVLO) with hysteresis
- Wide operating range: 15 to 30 Volts (VCC)
- Guaranteed performance over temperature -40°C ~ +110°C.
- · Safety agency certification
 - ✓ UL 1577 approved
 - ✓ VDE approved DIN EN/IEC60747-5-2
 - ✓ CQC GB4943.1, GB8898



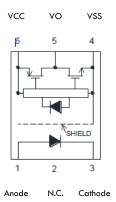




Applications

- IGBT/MOSFET gate drive
- Uninterruptible power supply (UPS)
- Industrial Inverter
- AC/Brushless DC motor drives
- · Switching power suppliers

Functional Diagram



Marking

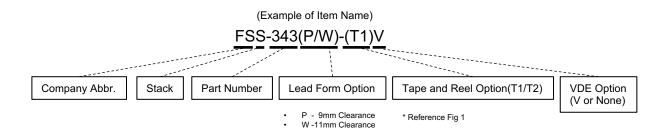


F : Company Abbr.
YY : Year date code
WW : 2-digit work week
343 : Part Number

H : Factory identification mark V : VDE Identification(Option)

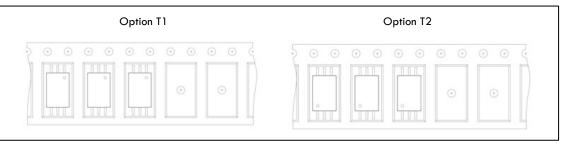
Ordering Information

To order, choose a part number from the part number column. Contact sales representative or authorized distributor for information.



Item Name	Lead Form Option	VDE Option	Tape and Reel Option	Packing(MOQ)			
FSS-343P-T1V	9mm Clearance	V					
FSS-343P-T1V	9mm Clearance	٧	0 11				
FSS-343W-T1	11mm Clearance					Option T1	
FSS-343W-T1	Timin Clearance			300K			
FSS-343P-T2V	Omm Claaranaa	V		300K			
FSS-343P-T2V	9mm Clearance	٧	0 10				
FSS-343W-T2	11mm Clearance		Option T2				
FSS-343W-T2	i illilli Glearance						

Fig 1



Truth Table

LED	V_{CC} - V_{SS} (Turn-ON, +ve going)	V_{CC} - V_{SS} (Turn-OFF, -ve going)	Vo
Off	0V to 30V	0V to 30V	Low
On	0V to 11.0V	0 - 9.5 V	Low
On	11.0 - 13.5 V	9.5 - 12 V	Transition
On	13.5 - 30 V	12 - 30 V	High

Note: A $0.1\mu F$ bypass capacitor must be connected between Pin 4 and 6.

Absolute Maximum Ratings

PARAMETER	SYMBOL	Min	Max	UNIT	Note
Storage Temperature	Tstg	-55	125	°C	-
Operating Temperature	Topr	-40	110	°C	-
Output IC Junction Temperature	TJ	-	125	°C	-
Total Output Supply Voltage	(VCC -VSS)	0	35	V	-
Average Forward Input Current	lF	-	20	mA	-
Reverse Input Voltage	VR	-	5	V	-
"High" Peak Output Current	IOH(PEAK)		4.0	Α	1
"Low" Peak Output Current	IOL(PEAK)		4.0	Α	1
Output Voltage	VO(PEAK)	-0.5	Vcc	V	-
Power Dissipation	PI	-	45	mW	-
Output IC Power Dissipation	PO	-	700	mW	-
Lead Solder Temperature	Tsol	-	260	°C	-

Note: Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely affect reliability.

Note 1: Exponential waveform. Pulse width \leq 10 μ s, f \leq 15 kHz

FSS-343 Series

Recommended Operating Conditions

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Operating Temperature	T _A	-40	110	°C
Supply Voltage	V_{cc}	15	30	V
Input Current (ON)	I _{F(ON)}	7	16	mA
Input Voltage (OFF)	$V_{\text{F(OFF)}}$	-3.0	0.8	V

Electrical Specifications

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
			INPUT CHA	RACTERIS	TICS		
Input Forward Voltage	VF	1.6	1.9	2.4	٧	IF=10mA	-
Input Forward Voltage Temperature Coefficient	ΔVF/ ΔΤ	-	-1.237	-	mV/°C	IF=10mA	-
Input Reverse Voltage	B∨R	5	-	-	٧	$IR = 10\mu A$	-
Input Threshold Current (Low to High)	IFLH	-	0.9	2	mA	V _O > 5V, I _O = 0A	-
Input Threshold Voltage (High to Low)	VFHL	0.8	-	-	٧	VCC = 30 V, VO < 5V	-
Input Capacitance	CIN	-	60	-	рF	f = 1 MHz, VF = 0 V	-
	_		OUTPUT CH	ARACTER	ISTICS		
High Level Supply Current	ICCH	-	1.70	3	mA	$I_F=10$ mA, VCC = 30V, VO = Open, Rg = 10Ω , Cg = 6 nF	_
Low Level Supply Current	ICCL	-	2.11	3	mA	$I_F = 0$ mA, VCC = 30V, VO = Open, Rg = 10Ω , Cg = 6 nF	_
High level output current	IOH	3.0	-	-	Α	$I_F = 10 \text{ mA, VCC} = 30V$ VO = VCC - 4	1
Low level output current	IOL	3.0	-	-	Α	$I_F = 0 \text{ mA}, VCC = 30V$ VO = VSS + 4	1
High level output voltage	VOH	29.7	29.88	-	٧	IF = 10mA, IO = -100mA	2,3
Low level output voltage	VOL	-	0.1	0.3	٧	I _F = 0 mA, IO = 100 mA	-
UVLO Threshold	VUVLO+	11.0	12.6	13.5	٧	VO > 5V, IF = 10 mA	-
	VUVLO-	9.5	11.2	12.0	V	VO < 5V, IF = 10 mA	

All Typical values at TA = 25° C and VCC – VSS = 30 V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Maximum pulse width = $10 \mu s$.

Note 2: In this test VOH is measured with a dc load current. When driving capacitive loads, VOH will approach VCC as IOH approaches zero amps.

Note 3: Maximum pulse width = 1 ms.

Switching Specifications

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition	Note
Propagation Delay Time to High Output Level	† _{PLH}	-	61.3	110			-
Propagation Delay Time to Low Output Level	† _{PHL}	-	74.5	110		$Rg = 10 \Omega,$ $Cg = 25 \text{ nF},$	-
Pulse Width Distortion	PWD	-	22	70	ns	f = 10kHz, Duty Cycle = 50%	-
Propagation Delay Difference Between Any Two Parts	PDD (t _{PHL} - t _{PLH})	-100	-	+100		IF = 10mA, VCC = 30V	-
Output Rise Time (10 to 90%)	† _r	-	20	-			-
Output Fall Time (90 to 10%)	† _f	-	15	-			-
Common mode transient immunity at high level output	CM _H	20	40	-	kV/μs	IF= 7 to 16mA VCC= 30V, TA= 25 °C, VCM= 1kV	1,2
Common mode transient immunity at low level output	CM _L	20	40	-	kV/μs	IF=0mA VCC= 30V, TA= 25 °C, VCM= 1kV	1,3

All Typical values at TA = 25° C and VCC – VSS = 30 V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1:Pin 2 needs to be connected to LED common.

Note 2: Common mode transient immunity in the high state is the maximum tolerable dVCM/dt of the common mode pulse, VCM, to assure that the output will remain in the high state (meaning VO > 15.0V).

Note 3: Common mode transient immunity in a low state is the maximum tolerable dVCM/dt of the common mode pulse, VCM, to assure that the output will remain in

a low state (meaning VO < 1.0V).

Isolation Characteristic

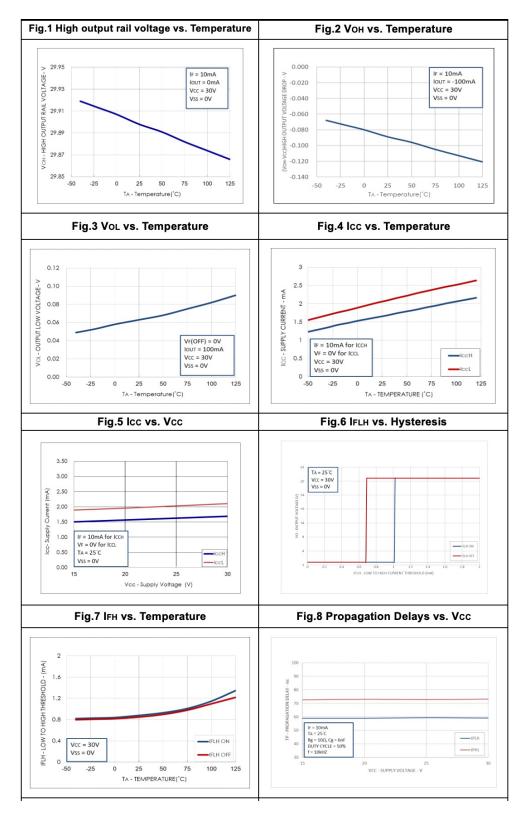
Parameter	Symbo	Device	Min.	Тур.	Max.	Unit	Test Condition	Note
Withstand Insulation	V _{ISO}	FSS-343P	5000	_		V	RH ≤ 40%-60%,	1,2
Test Voltage	VISO	FSS-343W	3000	-	_	V	$t = 1 \text{min}, T_A = 25 ^{\circ}\text{C}$	1,2
Input-Output Resistance	R _{I-O}	-	1	1012	ı	Ω	V _{I-O} = 500V DC	1

All Typical values at TA = 25° C and VCC – VSS = 30 V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Device is considered a two terminal device: pins 1, 2, 3 are shorted together and pins 4, 5, 6 are shorted together.

Note 2: According to UL1577, each photocoupler is tested by applying an insulation test voltage 6000VRMS for one second. This test is performed before the 100% production test for partial discharge.

Typical Performance Curves & Test Circuits



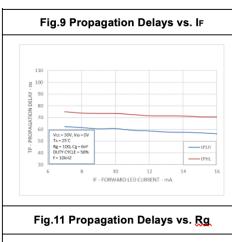
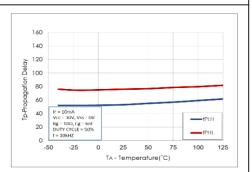


Fig.10 Propagation Delays vs. Temperature



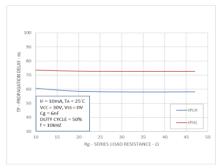


Fig.12 Propagation Delays vs. Cg

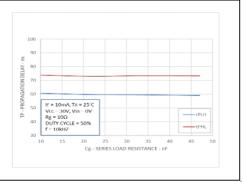
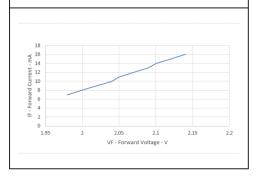
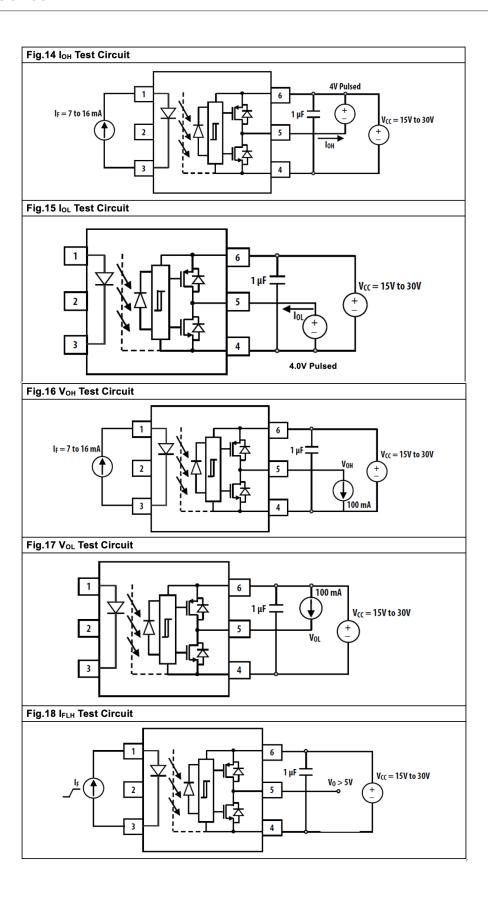
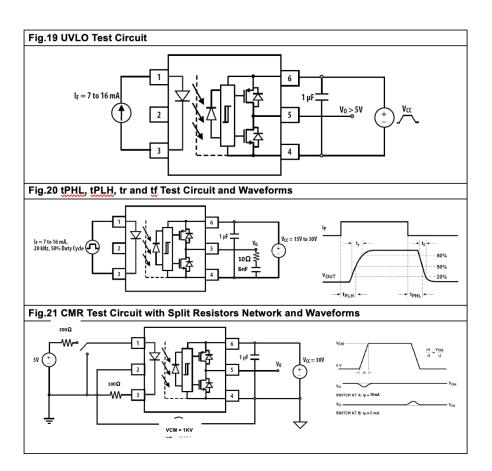


Fig.13 Input Current vs. Forward Voltage

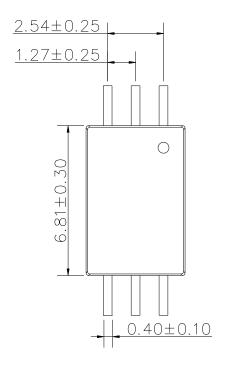


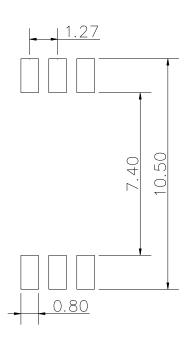


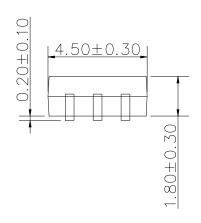


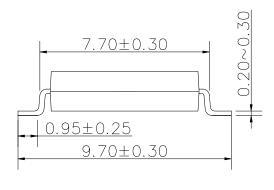
Package Outline Drawings

Surface Mount Lead Forming - P type Dimension





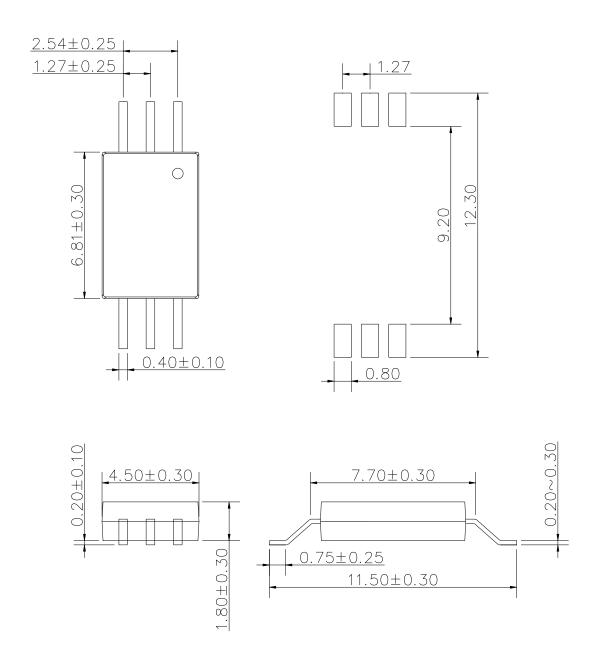




Dimensions in mm unless otherwise stated

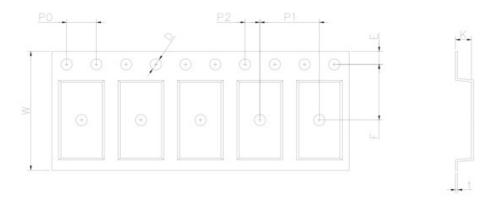
Package Outline Drawings

Surface Mount Lead Forming - W type Dimension



Dimensions in mm unless otherwise stated

Taping Dimensions



Dimension Symbol	D	Е	F	P0	P1	P2	t	W	K
P type	1.5±0.1	1.75±0.1	7.5±0.1	4.0±0.1	8.0±0.1	2.0±0.1	0.3±0.1	16.0±0.3	2.15±0.1
Dimension (mm)		0_0			0.020	2.020	0.020	.0.020.0	2.102011
W type	1.5±0.1	1.75±0.1	11.5±0.1	4.0±0.1	8.0±0.1	2.0±0.1	0.3±0.1	24.0±0.3	2.52±0.1
Dimension (mm)	1.J±0.1	1.7 J±0.1	11.J±0.1	4.010.1	0.0±0.1	Z.U±0.1	0.3±0.1	24.UIU.3	Z.JZIU. I

Dimensions in mm unless otherwise stated

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